

<b>Notification Number:</b>	20200921000	<b>Notification Date:</b>	Oct. 8, 2020
<b>Title:</b>	Datasheet for TMS320F2837xS, TMS320F2837xD, TMS320F2807x		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Change Type:</b>	Electrical Specification		

## Notification Details

### Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



**TMS320F28379S, TMS320F28378S, TMS320F28377S  
TMS320F28376S, TMS320F28375S, TMS320F28374S**

SPRS881I –AUGUST 2014 –REVISED JUNE 2020

Changes from March 3, 2020 to June 25, 2020 (from H Revision (March 2020) to I Revision)	Page
• <b>Global:</b> Changed "debug probe" to "JTAG debug probe".	<a href="#">1</a>
• <b>Section 1.1 (Features):</b> Updated "Functional Safety-Compliant" feature. Added "Safety-related certification" feature.	<a href="#">1</a>
• <b>Section 5.1 (Absolute Maximum Ratings):</b> Updated Input clamp current.	<a href="#">51</a>
• <b>Table 5-4 (Reset (XRS) Timing Requirements):</b> Updated $t_{w(RSL2)}$ .	<a href="#">62</a>
• <b>Section 5.11.5.1 (SDFM Electrical Data and Timing (Using ASYNC)):</b> Added WARNING about Mode 2 (Manchester Mode).	<a href="#">141</a>
• <b>Table 5-67 (SDFM Timing Requirements When Using Asynchronous GPIO (ASYNC) Option):</b> Added four parameters to Mode 2 section.	<a href="#">141</a>
• <b>Figure 5-58 (SDFM Timing Diagram – Mode 2):</b> Updated figure.	<a href="#">143</a>
• <b>Section 5.12.2.1 (I2C Electrical Data and Timing):</b> Updated NOTE about I2C module clock.	<a href="#">149</a>
• <b>Table 5-69 (I2C Timing Requirements):</b> Updated table.	<a href="#">149</a>
• <b>Table 5-70 (I2C Switching Characteristics):</b> Updated table.	<a href="#">149</a>
• <b>Figure 5-62 (I2C Timing Diagram):</b> Added figure.	<a href="#">150</a>
• <b>Figure 5-70 (SCI Block Diagram):</b> Updated figure.	<a href="#">162</a>
• <b>Figure 5-73 (SPI Master Mode External Timing (Clock Phase = 1)):</b> Updated Parameter 24.	<a href="#">169</a>
• <b>Table 5-80 (SPI Slave Mode Timing Requirements):</b> Updated Parameter 25, $t_{su(STEME)}$ .	<a href="#">170</a>
• <b>Table 6-2 (Addresses of Flash Sectors on F28379S, F28378S, F28377S, and F28375S):</b> Updated START ADDRESS and END ADDRESS of Flash ECC (Sector 14) to Flash ECC (Sector 27).	<a href="#">182</a>
• <b>Table 6-5 (Peripheral Registers Memory Map):</b> Added footnote about address overlap of PieCtrlRegs and Cla1SoftIntRegs.	<a href="#">185</a>
• <b>Section 6.15 (Functional Safety):</b> Updated section.	<a href="#">206</a>

**Changes from March 3, 2020 to June 25, 2020 (from L Revision (March 2020) to M Revision)**
**Page**

• <b>Global:</b> Changed "debug probe" to "JTAG debug probe".	<a href="#">1</a>
• <b>Section 1.1 (Features):</b> Updated "Functional Safety-Compliant" feature. Added "Safety-related certification" feature.	<a href="#">1</a>
• <b>Section 5.1 (Absolute Maximum Ratings):</b> Updated Input clamp current.	<a href="#">51</a>
• <b>Table 5-4 (Reset (XRS) Timing Requirements):</b> Updated $t_{w(RSL2)}$ .	<a href="#">62</a>
• <b>Section 5.11.5.1 (SDFM Electrical Data and Timing (Using ASYNC)):</b> Added WARNING about Mode 2 (Manchester Mode).	<a href="#">142</a>
• <b>Table 5-67 (SDFM Timing Requirements When Using Asynchronous GPIO (ASYNC) Option):</b> Added four parameters to Mode 2 section.	<a href="#">142</a>
• <b>Figure 5-58 (SDFM Timing Diagram – Mode 2):</b> Updated figure.	<a href="#">144</a>
• <b>Section 5.12.2.1 (I2C Electrical Data and Timing):</b> Updated NOTE about I2C module clock.	<a href="#">150</a>
• <b>Table 5-69 (I2C Timing Requirements):</b> Updated table.	<a href="#">150</a>
• <b>Table 5-70 (I2C Switching Characteristics):</b> Updated table.	<a href="#">150</a>
• <b>Figure 5-62 (I2C Timing Diagram):</b> Added figure.	<a href="#">151</a>
• <b>Figure 5-70 (SCI Block Diagram):</b> Updated figure.	<a href="#">163</a>
• <b>Figure 5-73 (SPI Master Mode External Timing (Clock Phase = 1)):</b> Updated Parameter 24.	<a href="#">170</a>
• <b>Table 5-80 (SPI Slave Mode Timing Requirements):</b> Updated Parameter 25, $t_{su(STE)S}$ .	<a href="#">171</a>
• <b>Table 6-5 (Peripheral Registers Memory Map):</b> Added footnote about address overlap of PieCtrlRegs and Cla1SoftIntRegs.	<a href="#">185</a>
• <b>Section 6.16 (Functional Safety):</b> Updated section.	<a href="#">207</a>

**Changes from March 3, 2020 to June 25, 2020 (from H Revision (March 2020) to I Revision)**
**Page**

• <b>Global:</b> Changed "debug probe" to "JTAG debug probe".	<a href="#">1</a>
• <b>Section 1.1 (Features):</b> Updated "Functional Safety-Compliant" feature. Added "Safety-related certification" feature.	<a href="#">1</a>
• <b>Section 5.1 (Absolute Maximum Ratings):</b> Updated Input clamp current.	<a href="#">36</a>
• <b>Table 5-5 (Reset (XRS) Timing Requirements):</b> Updated $t_{w(RSL2)}$ .	<a href="#">49</a>
• <b>Section 5.11.5.1 (SDFM Electrical Data and Timing (Using ASYNC)):</b> Added WARNING about Mode 2 (Manchester Mode).	<a href="#">122</a>
• <b>Table 5-64 (SDFM Timing Requirements When Using Asynchronous GPIO (ASYNC) Option):</b> Added four parameters to Mode 2 section.	<a href="#">122</a>
• <b>Figure 5-54 (SDFM Timing Diagram – Mode 2):</b> Updated figure.	<a href="#">124</a>
• <b>Section 5.12.2.1 (I2C Electrical Data and Timing):</b> Updated NOTE about I2C module clock.	<a href="#">130</a>
• <b>Table 5-66 (I2C Timing Requirements):</b> Updated table.	<a href="#">130</a>
• <b>Table 5-67 (I2C Switching Characteristics):</b> Updated table.	<a href="#">130</a>
• <b>Figure 5-58 (I2C Timing Diagram):</b> Added figure.	<a href="#">131</a>
• <b>Figure 5-66 (SCI Block Diagram):</b> Updated figure.	<a href="#">143</a>
• <b>Figure 5-69 (SPI Master Mode External Timing (Clock Phase = 1)):</b> Updated Parameter 24.	<a href="#">150</a>
• <b>Table 5-77 (SPI Slave Mode Timing Requirements):</b> Updated Parameter 25, $t_{su(STE)S}$ .	<a href="#">151</a>
• <b>Table 6-4 (Peripheral Registers Memory Map):</b> Added footnote about address overlap of PieCtrlRegs and Cla1SoftIntRegs.	<a href="#">159</a>
• <b>Section 6.15 (Functional Safety):</b> Updated section.	<a href="#">178</a>

The datasheet number will be changing.

Device Family	Change From:	Change To
TMS320F28379S, TMS320F28378S, TMS320F28377S, TMS320F28376S, TMS320F28375S, TMS320F28374S	SPRS881H	SPRS881I
TMS320F28379D, TMS320F28378D, TMS320F28377D, TMS320F28376D, TMS320F28375D, TMS320F28374D	SPRS880L	SPRS880M
TMS320F28076, TMS320F28075	SPRS902H	SPRS902I

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TMS320F28379S>

<http://www.ti.com/product/TMS320F28379D>

<http://www.ti.com/product/TMS320F28076>

**Reason for Change:**

To accurately reflect device characteristics.

**Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):**

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

**Changes to product identification resulting from this notification:**

None.

**Product Affected:**

TMS320F28075PTPQ	TMS320F28374SZWTT	TMS320F28376DZWTT	TMS320F28377SPZPS
TMS320F28075PTPS	TMS320F28374SZWTR	TMS320F28376SPTPS	TMS320F28377SPZPT
TMS320F28075PTPT	TMS320F28375DPTPS	TMS320F28376SPTPT	TMS320F28377SZWTQ
TMS320F28075PZPQ	TMS320F28375DPTPT	TMS320F28376SPZPS	TMS320F28377SZWTS
TMS320F28075PZPS	TMS320F28375DPZPS	TMS320F28376SPZPT	TMS320F28377SZWTT
TMS320F28075PZPT	TMS320F28375DZWTS	TMS320F28376SZWTS	TMS320F28378DPTPS
TMS320F28076PTPS	TMS320F28375DZWTT	TMS320F28376SZWTT	TMS320F28378SPTPS
TMS320F28076PZPS	TMS320F28375SPTPS	TMS320F28377DPTPQ	TMS320F28378SPZPS
TMS320F2807NPTPT	TMS320F28375SPTPT	TMS320F28377DPTPS	TMS320F28379DPTPQ
TMS320F28374DPTPS	TMS320F28375SPZPQ	TMS320F28377DPTPT	TMS320F28379DPTPS
TMS320F28374DPTPT	TMS320F28375SPZPQR	TMS320F28377DZWTQ	TMS320F28379DPTPT
TMS320F28374DZWTS	TMS320F28375SPZPS	TMS320F28377DZWTQR	TMS320F28379DZWTS
TMS320F28374DZWTT	TMS320F28375SPZPT	TMS320F28377DZWTS	TMS320F28379DZWTT
TMS320F28374SPTPS	TMS320F28375SZWTS	TMS320F28377DZWTT	TMS320F28379SPTPS
TMS320F28374SPTPT	TMS320F28375SZWTT	TMS320F28377SPTPQ	TMS320F28379SPTPT
TMS320F28374SPZPS	TMS320F28376DPTPS	TMS320F28377SPTPS	TMS320F28379SPZPS
TMS320F28374SPZPT	TMS320F28376DPTPT	TMS320F28377SPTPT	TMS320F28379SPZPT
TMS320F28374SZWTS	TMS320F28376DZWTS	TMS320F28377SPZPQ	TMS320F28379SZWTS
TMS320F28379SZWTT			

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