


PCN Number:	20200409000	PCN Date:	May 1, 2020
Title:	Datasheet for ISO7220A-Q1, ISO7221A-Q1, ISO7221C-Q1		
Customer Contact:	PCN Manager	Dept:	Quality Services
Proposed 1st Ship Date:	Nov 1, 2020		
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process
Notification Details			
Description of Change:			
<p>Texas Instruments Incorporated is announcing a change. The product datasheet(s) is being updated as summarized below.</p>			
		ISO7220A-Q1 ISO7221A-Q1 ISO7221C-Q1	
<small>SLLS965D –JULY 2009–REVISED APRIL 2020</small>			

Changes from Revision C (May 2012) to Revision D

Page

• Made editorial and cosmetic changes throughout the document	1
• Change standard names From: 'IEC 60747-5-2 (VDE 0884, Rev 2), IEC 61010-1' To: 'DIN VDE V 0884-11:2017-01, DIN EN 61010-1' and add 'IEC 62368-1' in FEATURES	1
• Updated REGULATORY INFORMATION table	2
• Changed V_I voltage rating From: '-0.5 V to 6 V' To: '-0.5 V to $V_{CC} + 0.5 V$ ' in ABSOLUTE MAXIMUM RATINGS table	3
• Added the following note to V_I parameter: 'Maximum voltage must not exceed 6V' in ABSOLUTE MAXIMUM RATINGS table.....	3
• Deleted typical values (TYP) for 'Input pulse width' and 'Signaling rate' specifications in RECOMMENDED OPERATING CONDITIONS table.....	3
• Added 'Ambient temperature' specification in RECOMMENDED OPERATING CONDITIONS table	3
• Changed 'Propagation delay' maximum (MAX) limit for ISO722xA From: 480 ns To: 600 ns in SWITCHING CHARACTERISTICS at $V_{CC1} = V_{CC2} = 5 V \pm 10\%$	4
• Changed 'Pulse-width distortion' maximum (MAX) limit for ISO722xA From: 14 ns To: 18 ns in SWITCHING CHARACTERISTICS at $V_{CC1} = V_{CC2} = 5 V \pm 10\%$	4
• Changed 'ISO722xA' to 'ISO7220A' and deleted 'ISO722xC' row from 'Channel-to-channel output skew' specification in SWITCHING CHARACTERISTICS at $V_{CC1} = V_{CC2} = 5 V \pm 10\%$	4
• Changed 'Propagation delay' maximum (MAX) limit for ISO722xA From: 480 ns To: 585 ns in SWITCHING CHARACTERISTICS at $V_{CC1} = 5 V \pm 10\%$, $V_{CC2} = 3.3 V \pm 10\%$	5
• Changed 'Pulse-width distortion' maximum (MAX) limit for ISO722xA From: 14 ns To: 18 ns in SWITCHING CHARACTERISTICS at $V_{CC1} = 5 V \pm 10\%$, $V_{CC2} = 3.3 V \pm 10\%$	5
• Changed 'ISO722xA' to 'ISO7220A' and deleted 'ISO722xC' row from 'Channel-to-channel output skew' specification in SWITCHING CHARACTERISTICS at $V_{CC1} = 5 V \pm 10\%$, $V_{CC2} = 3.3 V \pm 10\%$	5
• Changed 'Propagation delay' maximum (MAX) limit for ISO722xA From: 480 ns To: 605 ns in SWITCHING CHARACTERISTICS at $V_{CC1} = 3.3 V \pm 10\%$, $V_{CC2} = 5 V \pm 10\%$	6
• Changed 'Pulse-width distortion' maximum (MAX) limit for ISO722xA From: 18 ns To: 22 ns in SWITCHING CHARACTERISTICS at $V_{CC1} = 3.3 V \pm 10\%$, $V_{CC2} = 5 V \pm 10\%$	6
• Changed 'ISO722xA' to 'ISO7220A' and deleted 'ISO722xC' row from 'Channel-to-channel output skew' specification in SWITCHING CHARACTERISTICS at $V_{CC1} = 3.3 V \pm 10\%$, $V_{CC2} = 5 V \pm 10\%$	6
• Changed 'Propagation delay' maximum (MAX) limit for ISO722xA From: 485 ns To: 610 ns in SWITCHING CHARACTERISTICS at $V_{CC1} = V_{CC2} = 3.3 V \pm 10\%$	7
• Changed 'Pulse-width distortion' maximum (MAX) limit for ISO722xA From: 18 ns To: 22 ns in SWITCHING CHARACTERISTICS at $V_{CC1} = V_{CC2} = 3.3 V \pm 10\%$	7
• Changed 'ISO722xA' to 'ISO7220A' and deleted 'ISO722xC' row from 'Channel-to-channel output skew' specification in SWITCHING CHARACTERISTICS at $V_{CC1} = V_{CC2} = 3.3 V \pm 10\%$	7
• Changed 'Tracking resistance' TEST CONDITIONS From: DIN IEC 60112 / VDE 0303 Part 1 To: DIN EN 60112 (VDE 0303-11) in IEC PACKAGE CHARACTERISTICS table	9
• Deleted 'IEC 60747-5-2' from INSULATIONS CHARACTERISTICS table title.....	9
• Added 'Maximum withstanding isolation voltage' specification of 2500 V_{RMS} in INSULATION CHARACTERISTICS table	9
• Deleted ' θ_{JC} ' and 'per IEC 60747-5-2' from Figure 6 title.	11

The datasheet number will be changing.

Device Family	Change From:	Change To:
ISO7220A-Q1, ISO7221A-Q1, ISO7221C-Q1	SLLS965C	SLLS965D

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/ISO7220A-Q1>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

Electrical specification performance changes as indicated above.

Changes to product identification resulting from this PCN:			
None.			
Product Affected:			
ISO7220AQDRQ1	ISO7221AQDRQ1	ISO7221CQDRQ1	

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